



Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				C mplete if Known	
Sheet	1	of	3	Application Number	10/734,928
				Filing Date	December 12, 2003
				First Named Inventor	Alexander Kalnitsky
				Art Unit	2814 2823
				Examiner Name	Unassigned William M. Bresl
				Attorney Docket Number	55123P231D

U.S. PATENT DOCUMENTS

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
9/VB		US-4,157,269	06-05-1979	Ning, et al.	4 34/354
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9/VB		US-5,061,652	10-29-1991	Bender Nagel, et al.	4 38/184
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Examiner Signature	William M. Brewster	Date Considered	7 JUL 87
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				Attorney Docket Number	55123P231D
Sheet	2	of	3		

Examiner Signature	William M. Brewster	Date Considered	July 84
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Sheet	3	of	3	Attorney Docket Number	55123P231D

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No'	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T?
WB		STANLEY WOLF, et al., Silicon Processing for the VLSI ERA, Lattice Press, Sunset Beach, CA pgs. 191-194, 1986.	
WB		S.M. SZE, VLSI Technology, pgs. 51-129 & 448-461, 1983, McGraw-Hill Book Company	
WB		M. RACANELLI, et al., Ultra High Speed SiGe NPN for Advanced BiCMOS Technology, Siliocon RF Platform Technologies, Conexant Systems, Inc. Newport Beach, CA.	

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